



Express Mail Label No. EL 451 599 071 US

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

re application of:

Aronson *et al.*

Serial No. 09/777,917

Filed: February 5, 2001

For: *Integrated Memory Mapped Controller
Circuit for Fiber Optics Transceiver*

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DEC 16 2002

Technology Center 2600

Confirmation No. 6191

Group Art Unit: 2633

Examiner: Christina Leung

Attorney Docket No. 9775-0052-999

#7/B
L. mclanahan
12-18-02

SECOND PRELIMINARY AMENDMENT

Assistant Commissioner for Patents
Washington, D.C. 20231

Dear Sir:

This Amendment is a second preliminary amendment submitted prior to receipt of a first office action.

IN THE CLAIMS:

A marked-up version of the amended claims can be found in *Appendix A*.

1. (Amended) A single-chip integrated circuit for controlling an optoelectronic transceiver having a laser transmitter and a photodiode receiver, comprising:
memory, including one or more memory arrays for storing information related to the transceiver;
analog to digital conversion circuitry for receiving a plurality of analog signals from the laser transmitter and photodiode receiver, converting the received analog signals into digital values, and storing the digital values in predefined locations within the memory;
control circuitry configured to generate control signals to control operation of the laser transmitter in accordance with one or more values stored in the memory;
an interface for allowing a host to read directly from and write directly to locations within the memory; and